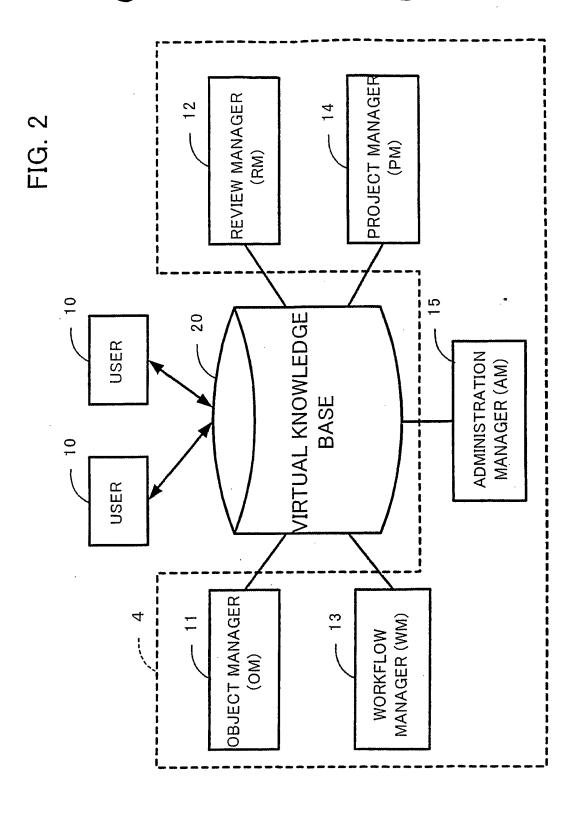
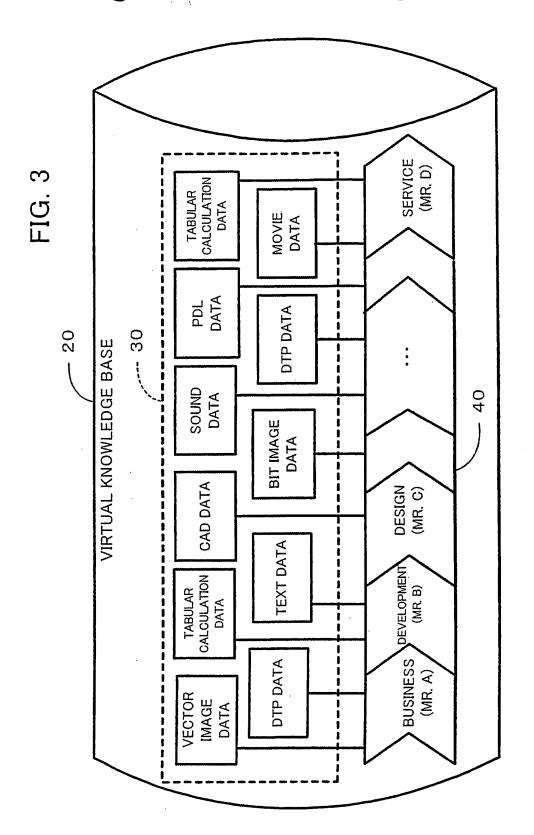
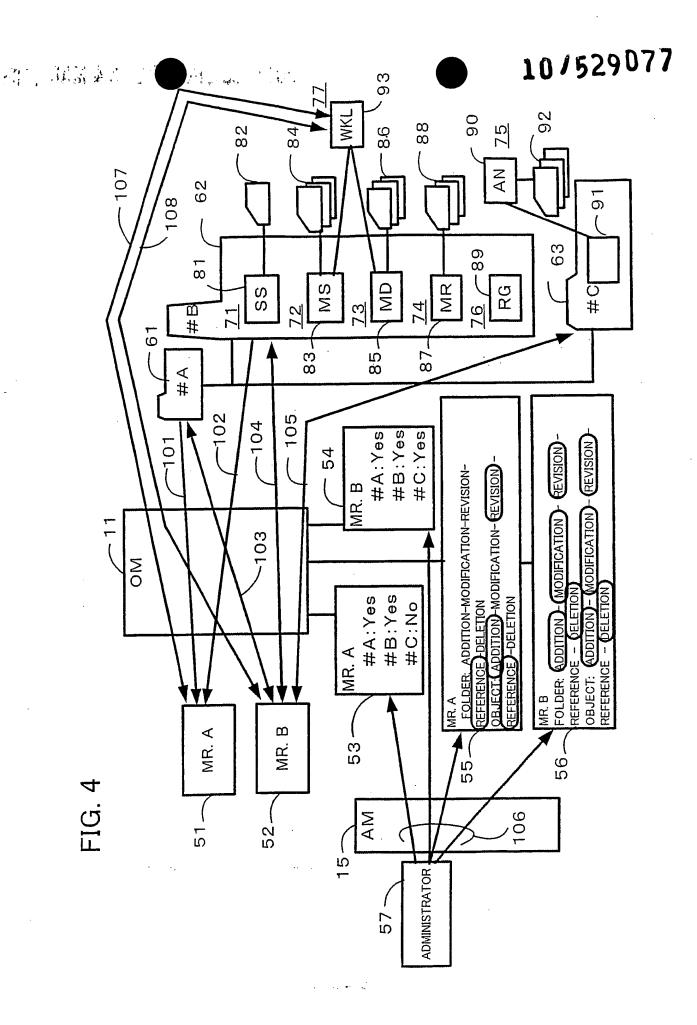


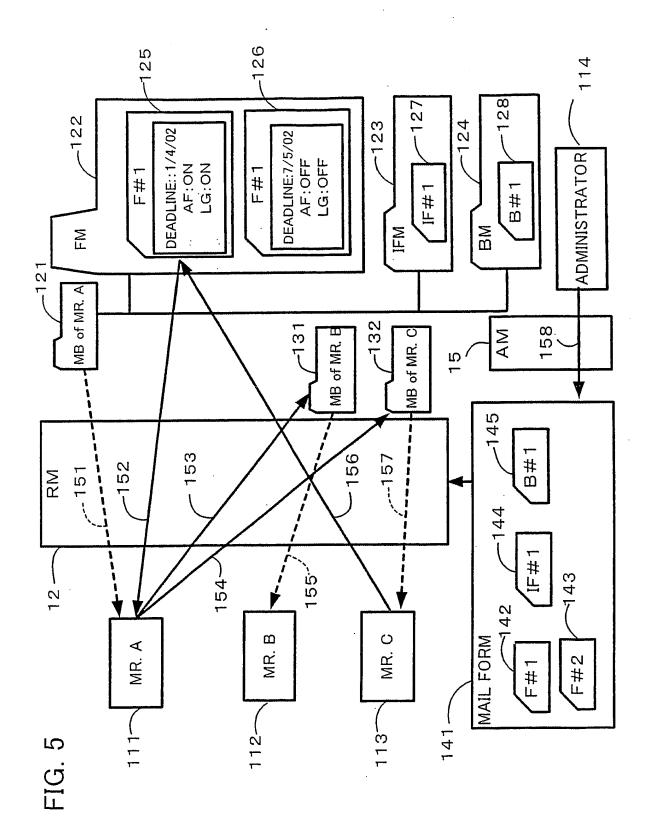
Best Available Copy

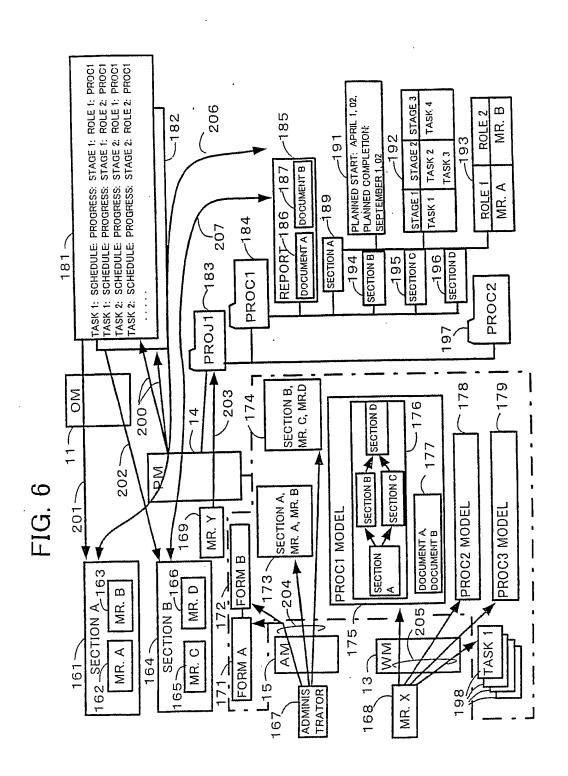




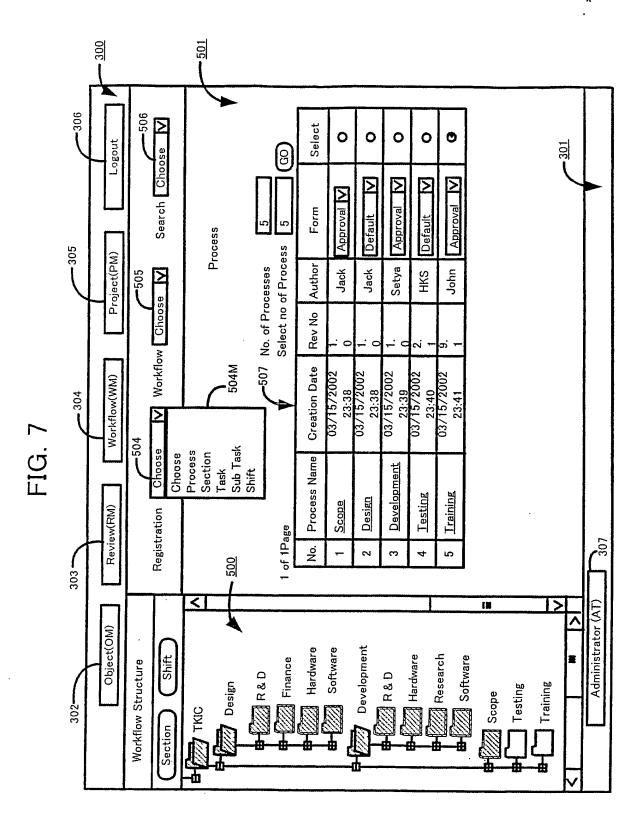
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FIG. 8

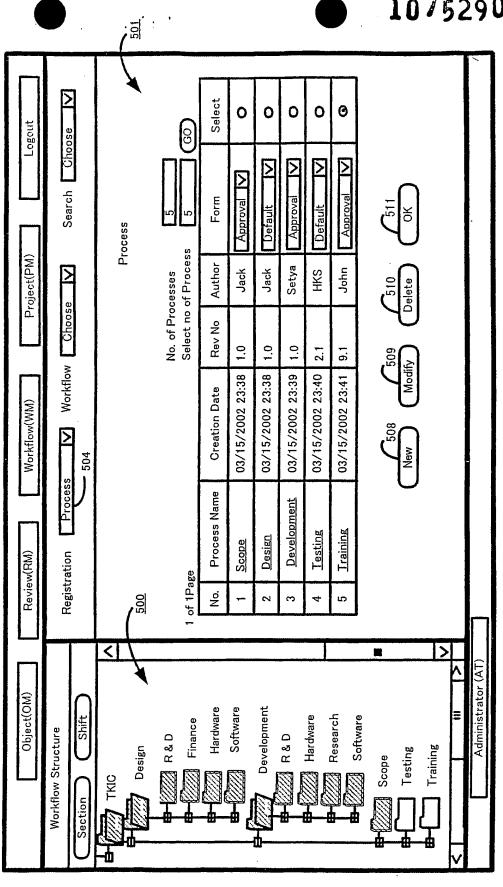


FIG. 9

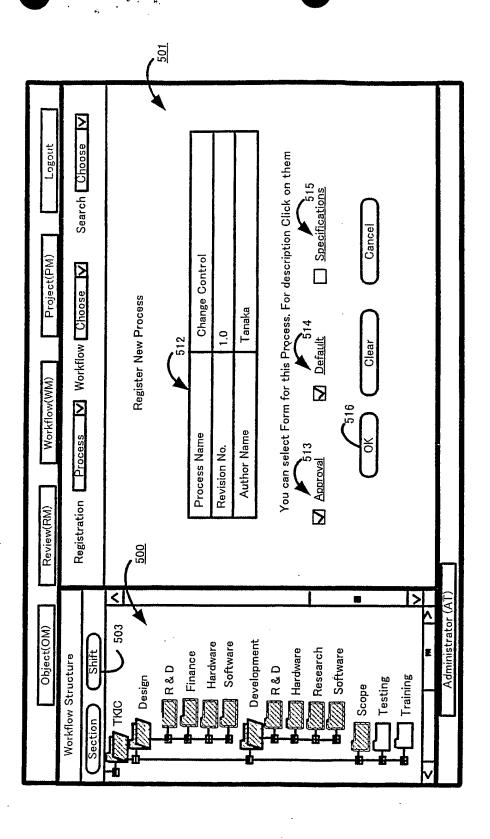


FIG. 10

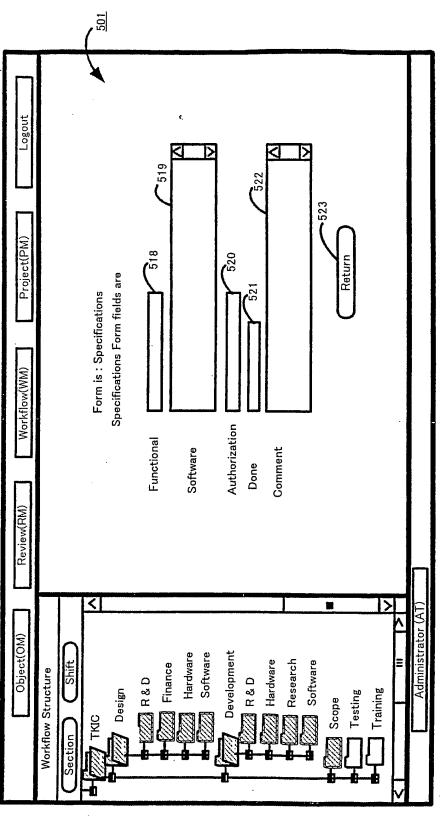


FIG. 11

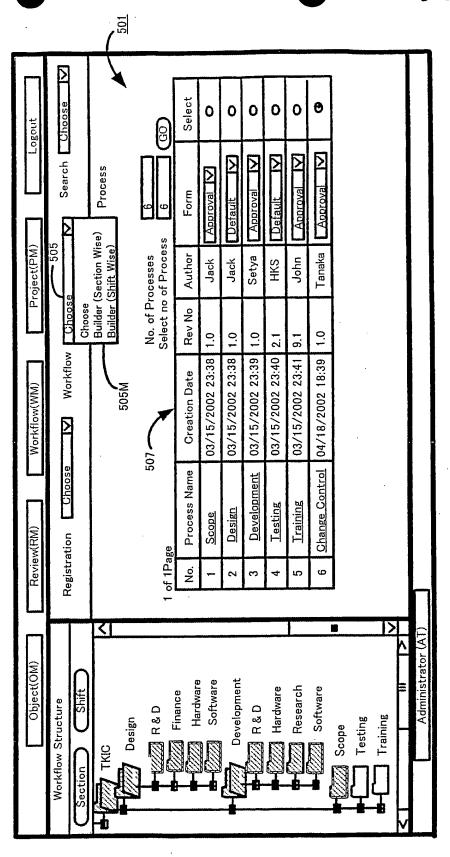
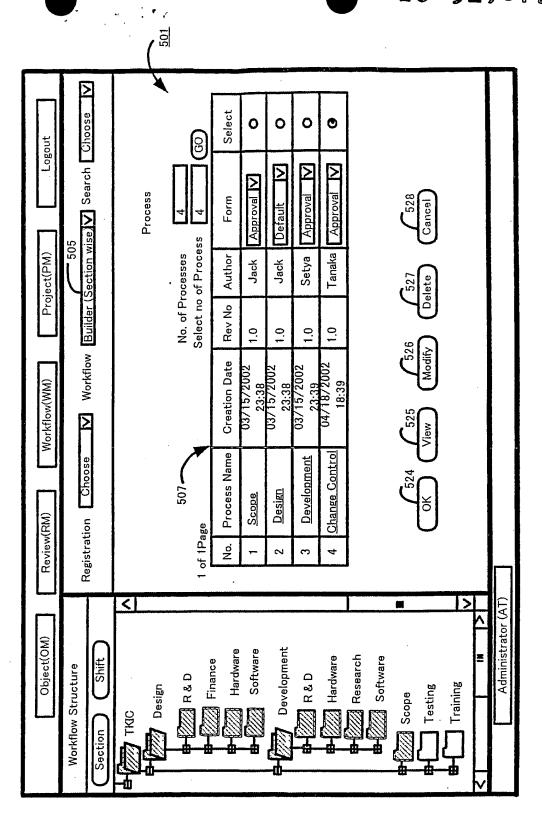


FIG. 12



501 Attribute Converge 539 Logout Selected Action Conv 10 537 Project(PM) 535 -540 Return Click on the section to see the attribute Workflow(WM) Change Control 532 Selected Process: Review(RM) Finance Hardware Software Quality test Object(OM) Software Hardware Development Finance Hardware 器 Research Software Workflow Structure R&D R&D Design Training Testing Scope

iG. 13

FIG. 14

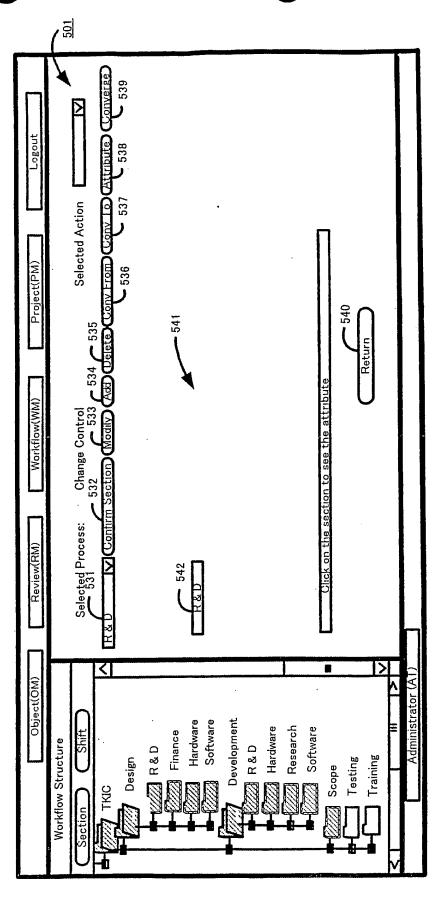
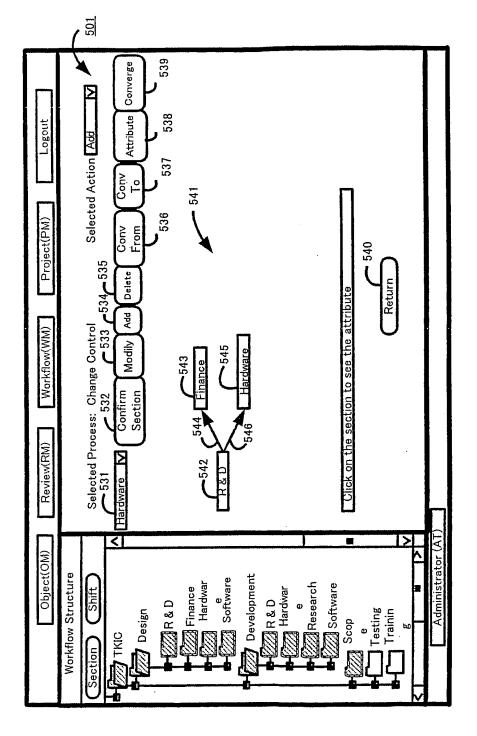


FIG. 15



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501 Converge Selected Action Conv to 🔽 Logout Conv To Attribute 541 537 Project(PM) 536 Conv From - 540 535 Return Modily Add Delete 549 533 6534 -548 Click on the section to see the attribute Workflow(WM) Change Control -545 **-**543 Confirm - 532 Selected Process: 531 Review(RM) - 542 Quality test Administrator (AT) < Object(OM) Development Hardware Software Research Software Software Shift Hardware 区 Finance Workflow Structure K & D Testing Training Design Scope

-1**G**. 16

FIG. 17

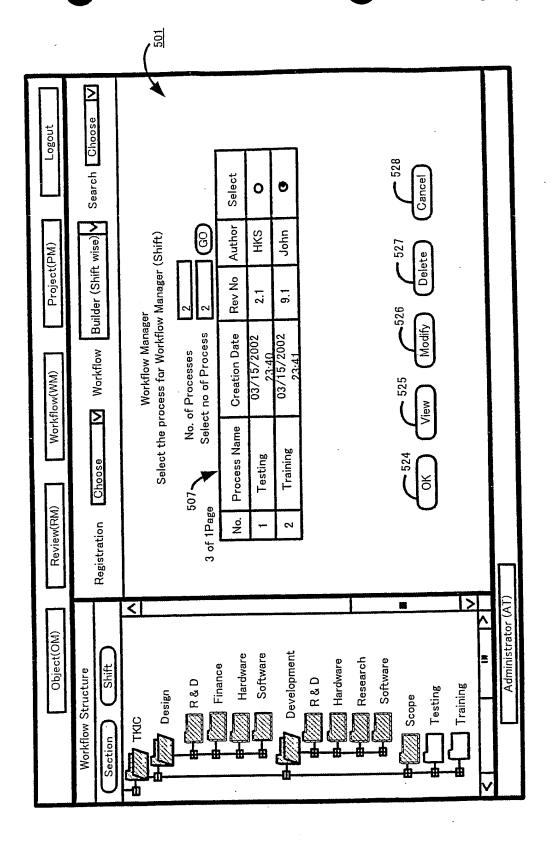


FIG. 18

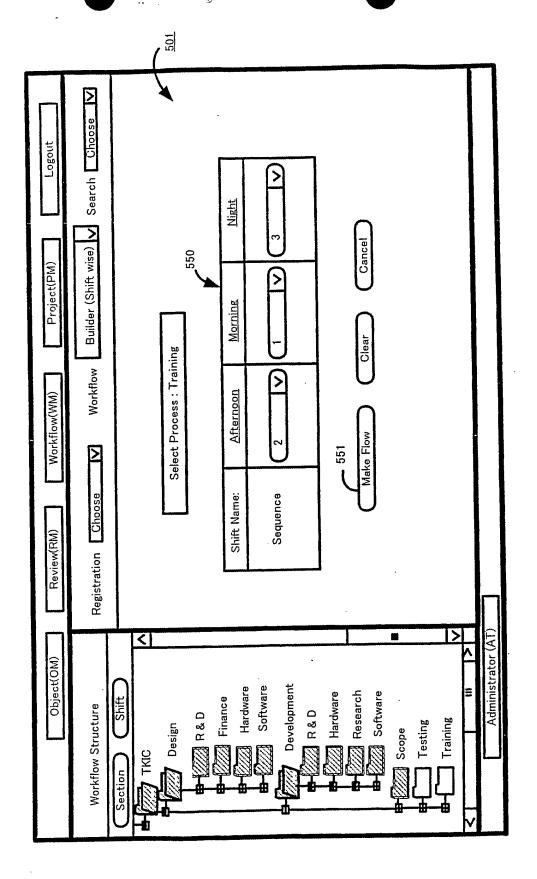
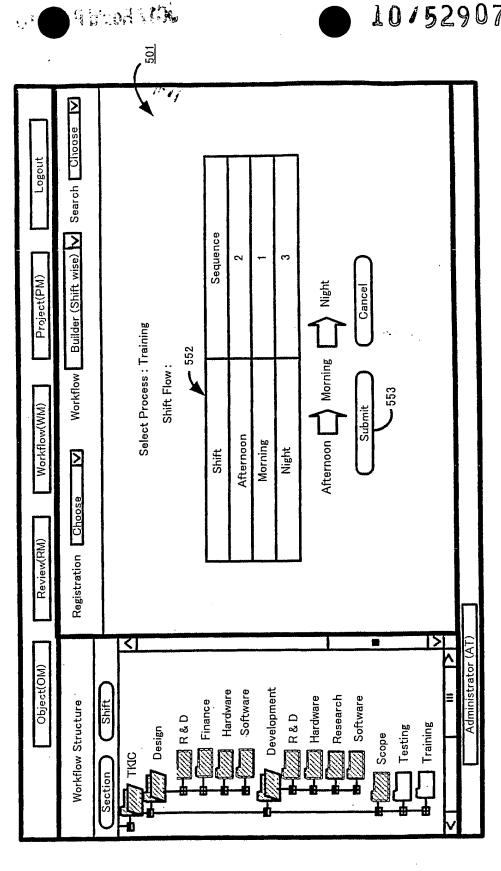


FIG. 19



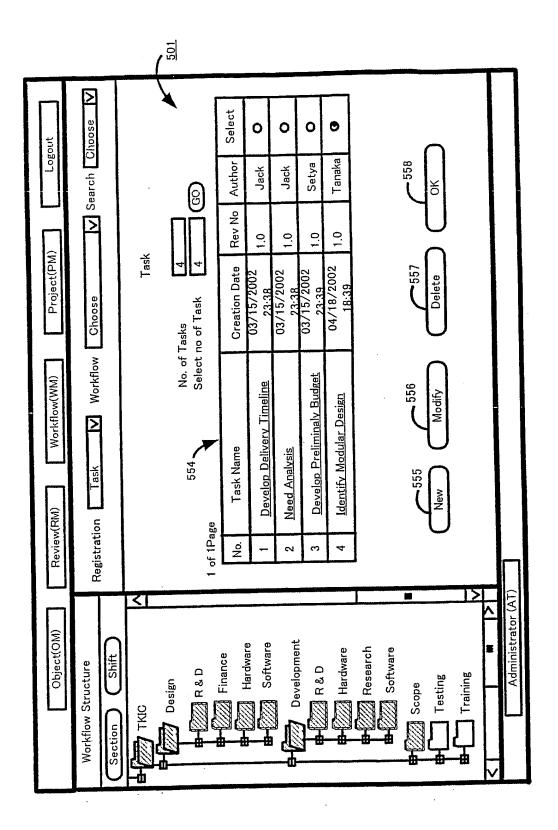
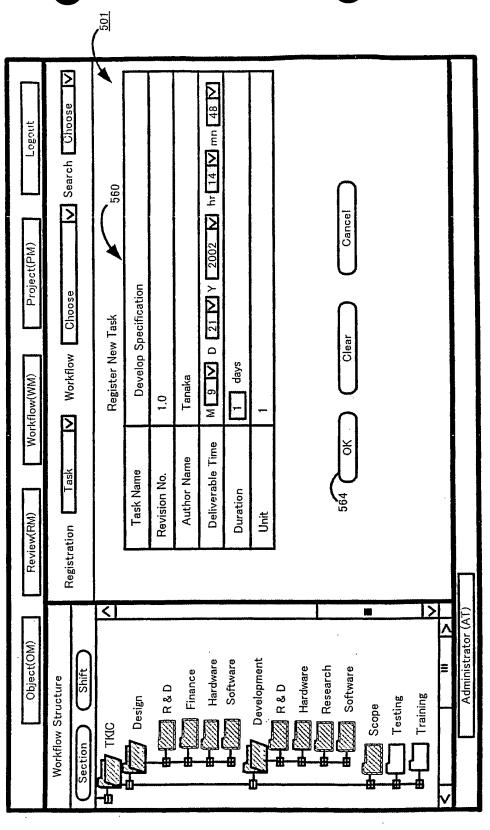
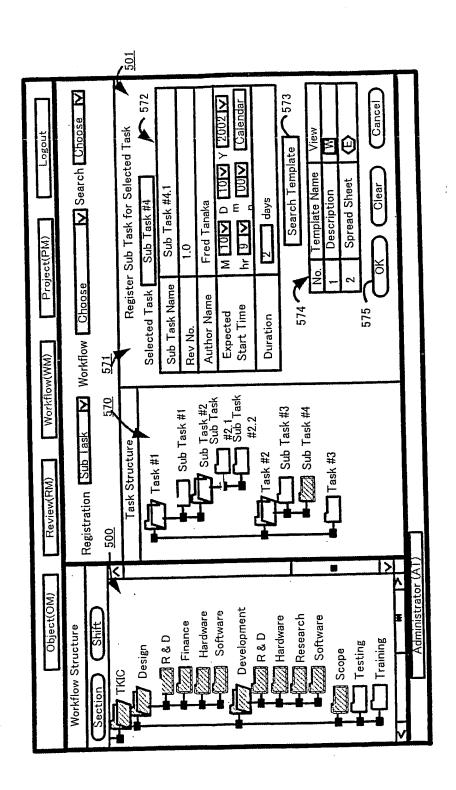


FIG. 21



Light Control





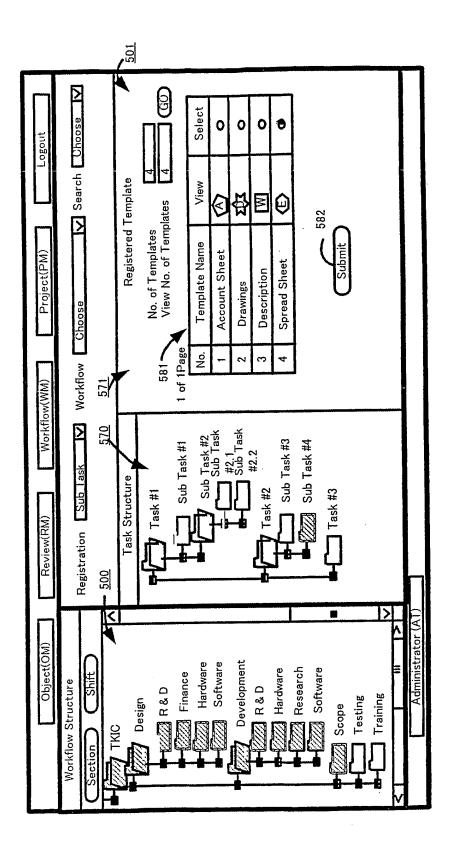


FIG. 24

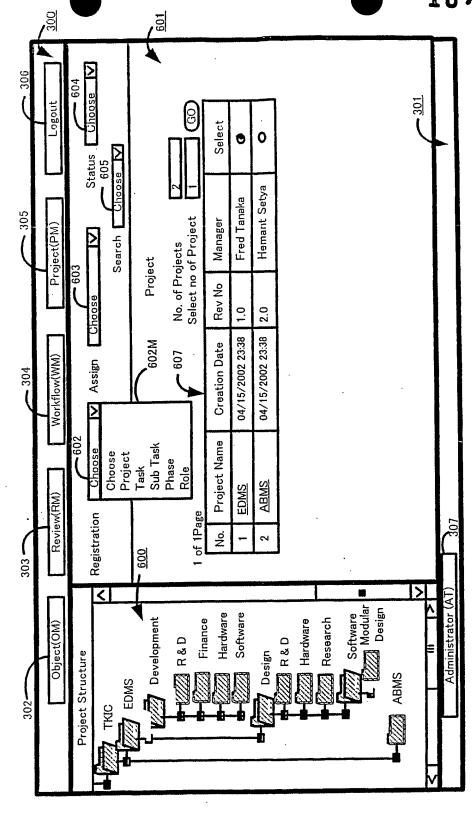
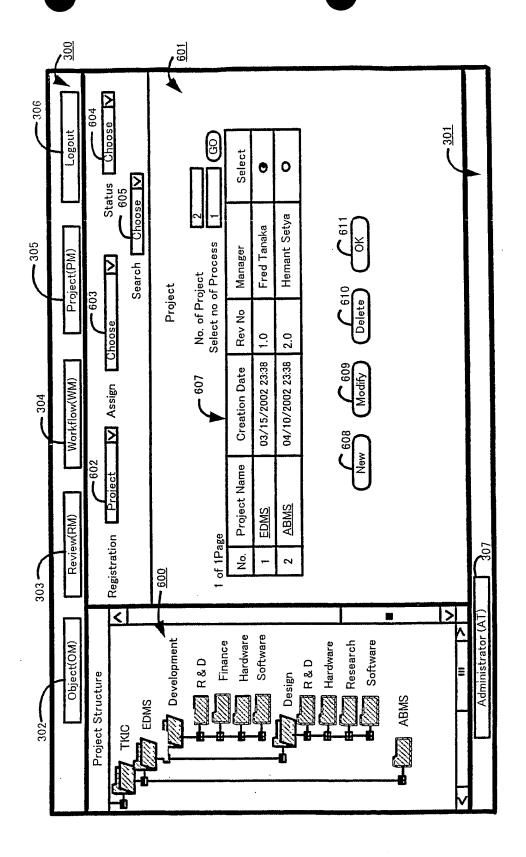


FIG. 25



- 1

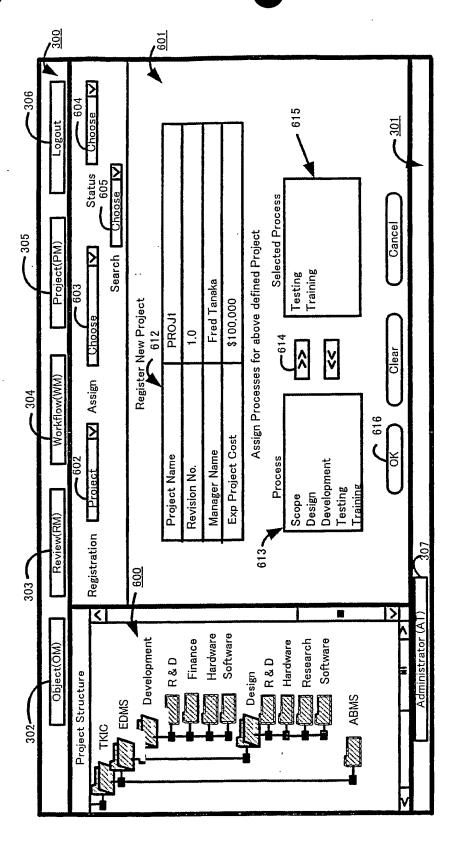
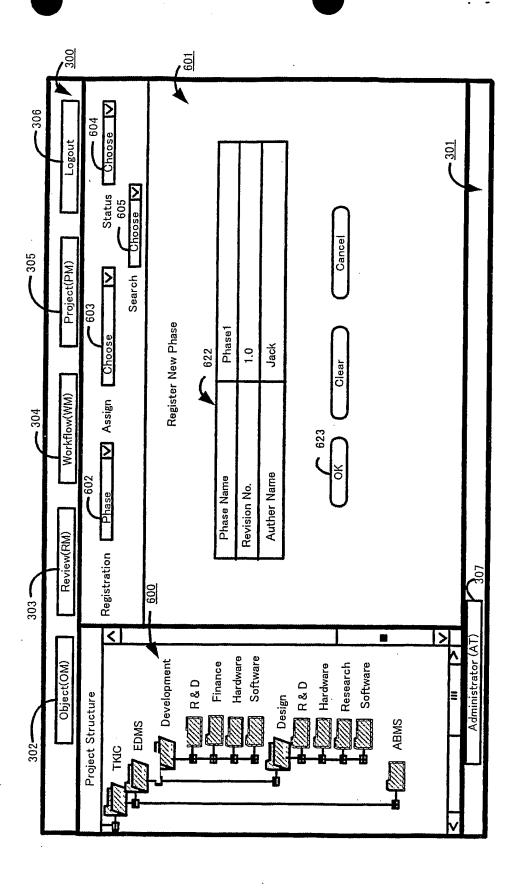


FIG. 26

Select 0 305 Jack Jack Auther Jack No. of Phases Select no of Phase Phase Rev No 04/15/2001 23:40 1.0 04/15/2001 23:50 2.0 04/15/2001 23:55 2.0 Creation Date - 617 ✓ Assign FIG. 27 Phase Name Midstream Intrim Final Registration ŝ 009 303 Hardware Software Finance Development Research Software Hardware R&D Project Structure ABMS ABMS

FIG. 28



**/** 301 Status 605 Choose V 625 1 624 **Expected Project Cost** \$30,000 305 Search Processes in this Project Selected Project: EDMS Details of Project Process Name Development V Assign Choos Fred Tanaka Design Manager 304 Rev No Registration Choose Creation Date 03/15/2002 23:38 ģ 009 Hardware Software Development Finance 🖾 Research Hardware Software M R&D R&D Design Project Structure ABMS ABMS 302

FIG. 29

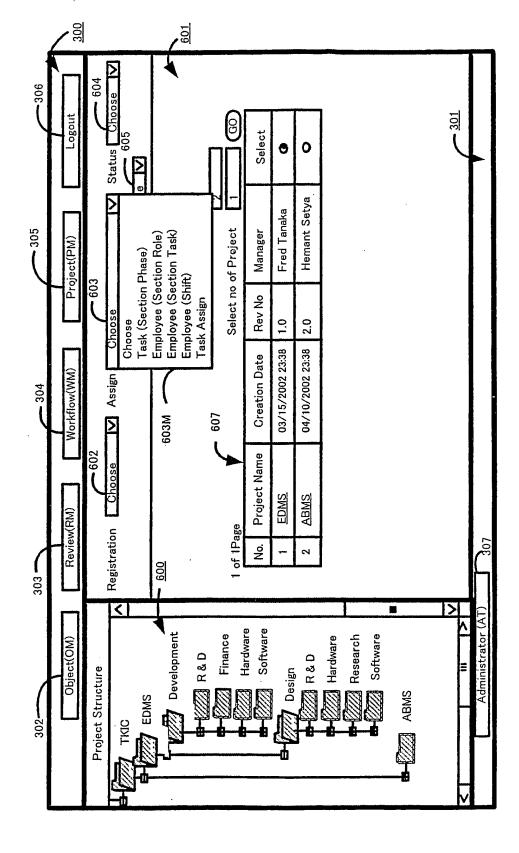
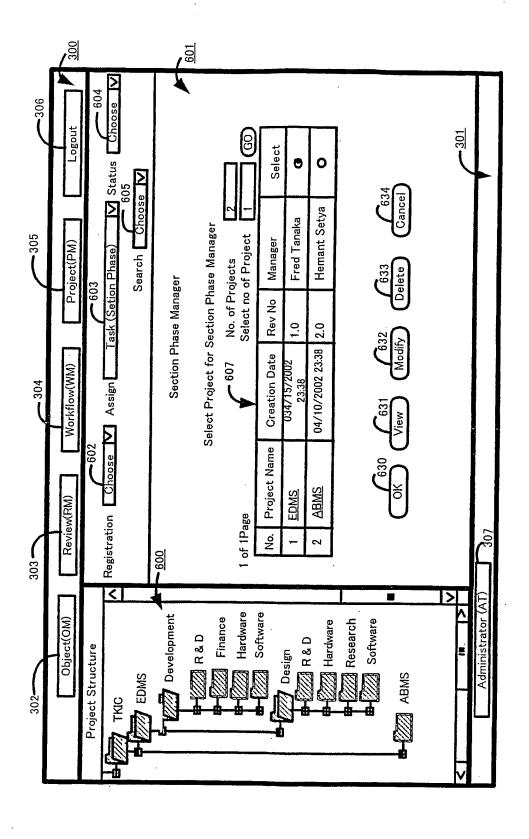


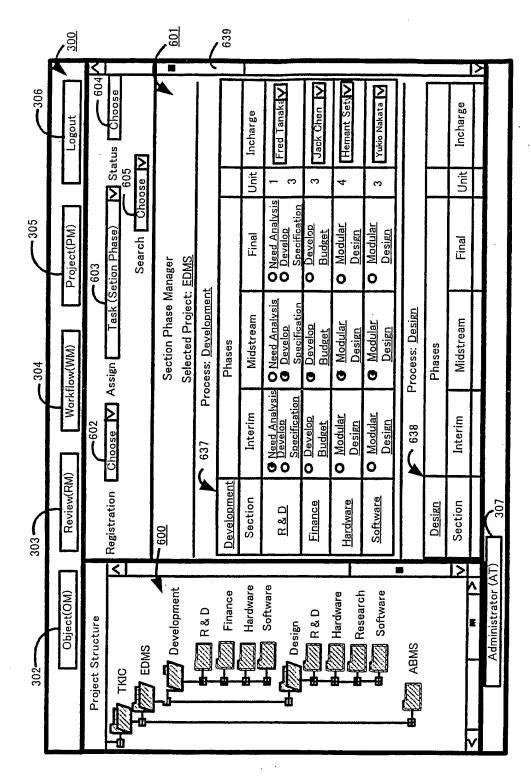
FIG. 3

FIG. 3



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Administrator (AT)

FIG. 33

<

Object(OM)

302

Project Structure

Development

7 EDMS

Hardware Software

Hardware Research

Design

Software

ABMS ABMS

Finance

R&D

604 Expected End Time Expected End Time Open Open Open Open V Status Start Time Expected Start Time Expected Open Open Open Open 305 Fred Tanaka 🗸 Fred Tanaka 🗸 Yukio Nakata 🗸 Hemant Setyl Registration Choose V Assign Employee (Section Task) Project(PM) Search Reviewer Reviewer Selected Project: EDMS Section Task Manager - 603 Process: Development Process: Design Employee Employee Workflow(WM) 304 Open Open Open Specification Develop Budget 643 Need Analysis 644 Modular -602 Modular Task Task Design <u>Uevelop</u> Design Review(RM) 307 Section Section Hardware Software Finance R&D 909 Hardware 7 Development Software Finance Research Software Hardware Object(OM) R&D R&D Design Project Structure 'EDMS ABMS ABMS 302

601

FIG. 34

FIG. 35

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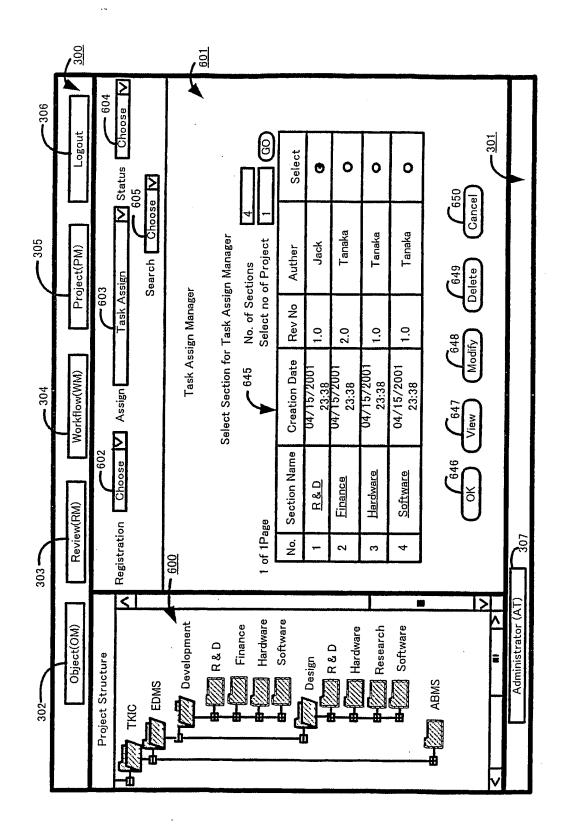
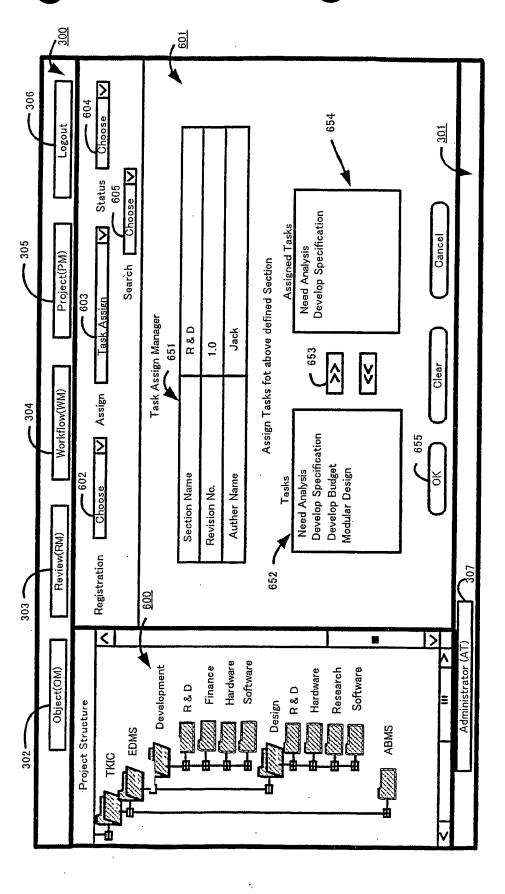


FIG. 36



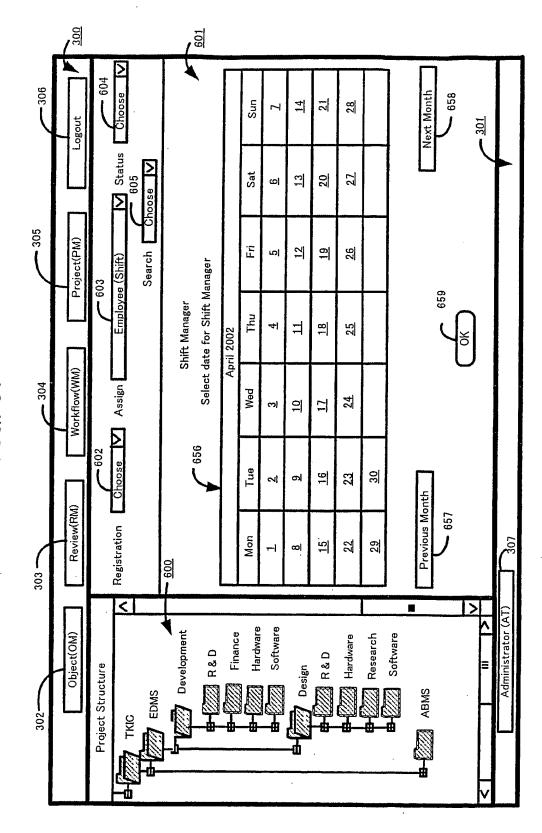
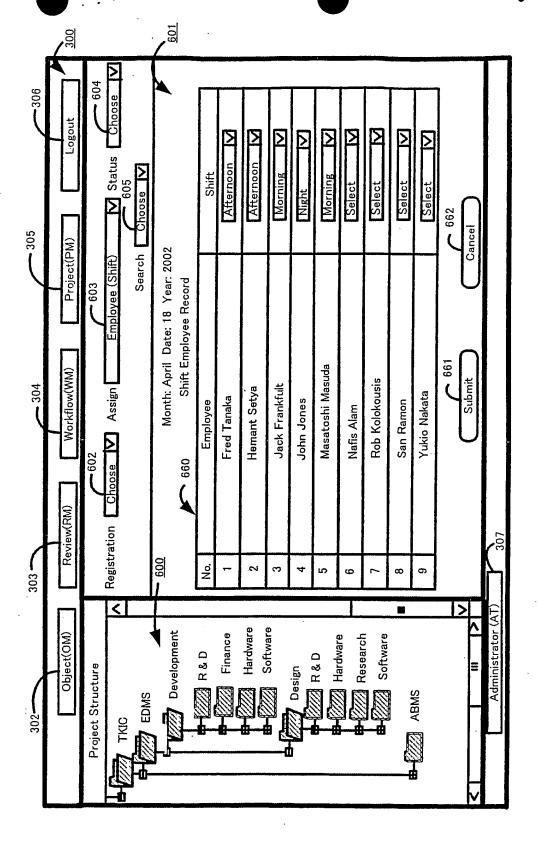


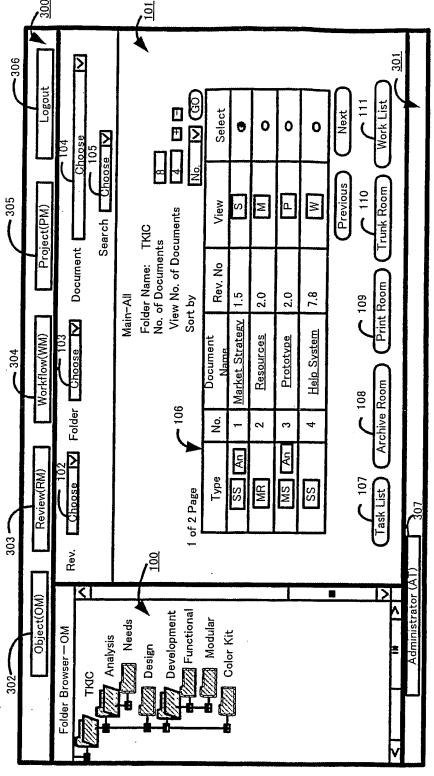
FIG. 37

FIG. 38

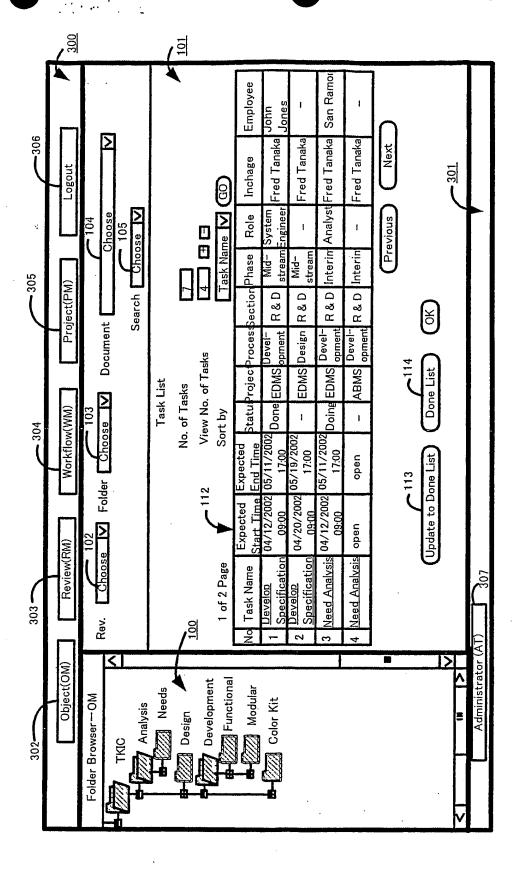


601 Status 605 18/4/2002 18/4/2002 18/4/2002 18/4/2002 18/4/2002 305 Project(PM) Search Month: April Date: 18 Year: 2002 Employee (Shift Shift Employee Record Afternoon Afternoon Morning Shift Morning Night orkflow(WM) Registration Choose V Assign 304 Masatoshi Masuda - 602 Jack Frankfult Hemant Setya 663 Fred Tanaka John Jones Employee 303 009 Š. ລ 4 Software Hardware Development Finance Object(OM) Hardware 🛭 Research Software R&D Z R&D Project Structure Design EDMS ABMS ABMS 302

FIG. 39







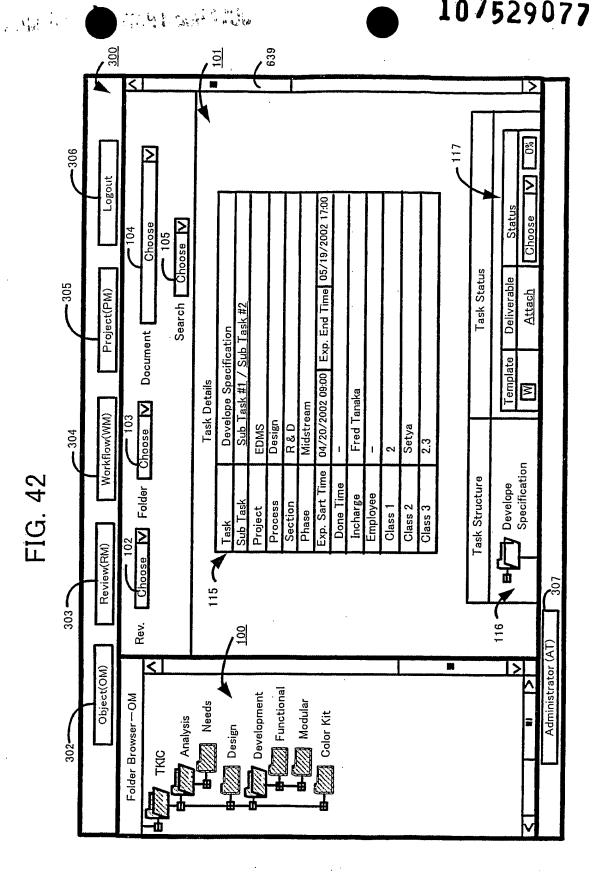
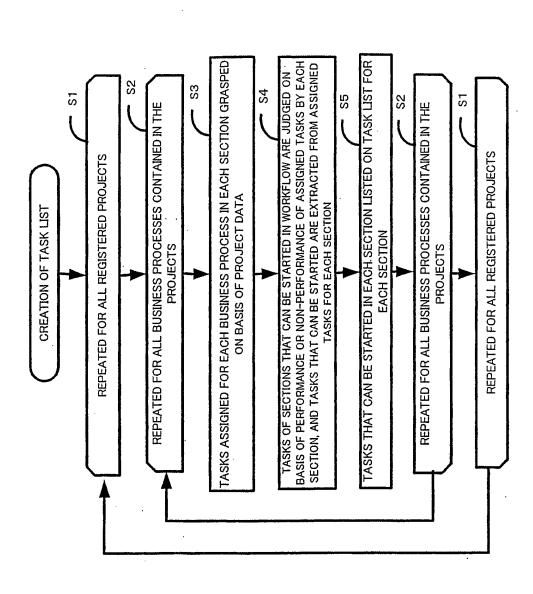


FIG. 43

FIG. 44





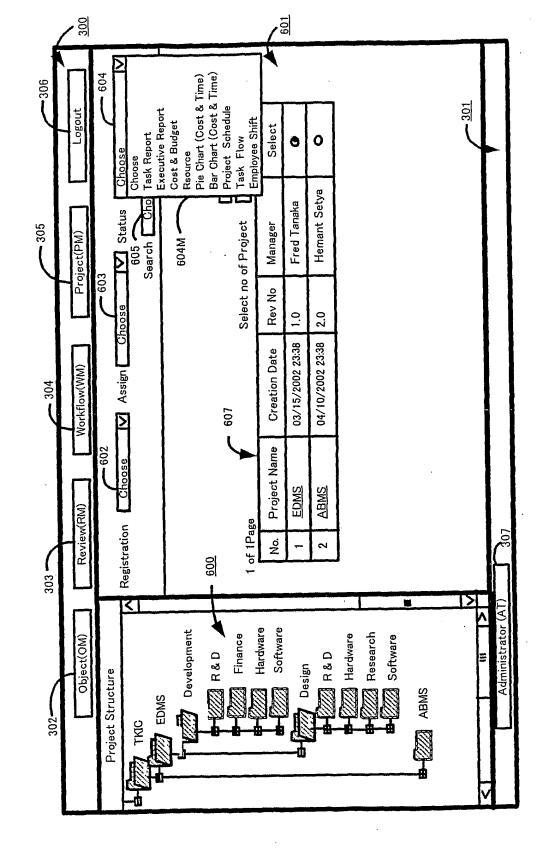
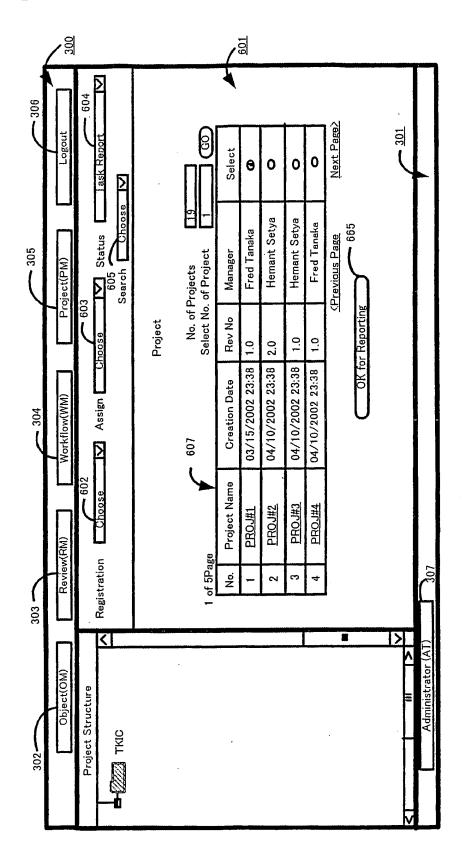
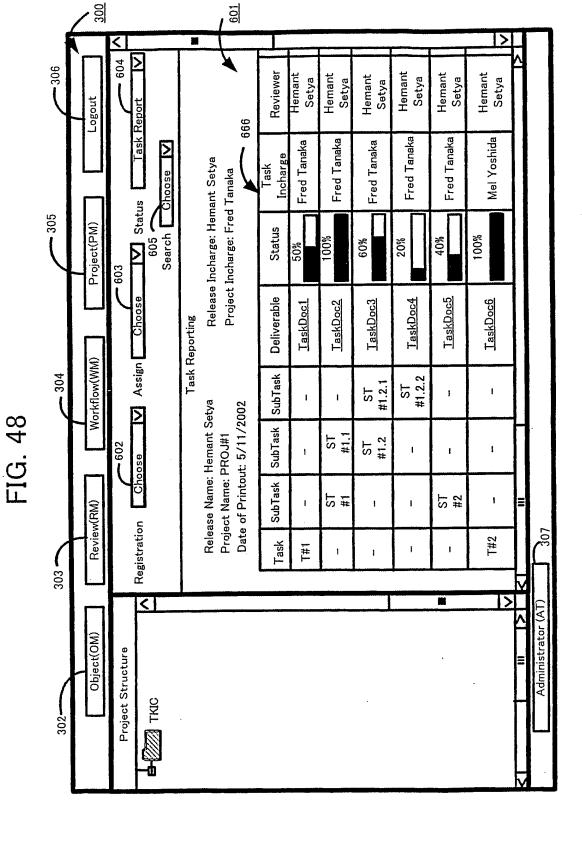


FIG. 46

FIG.47



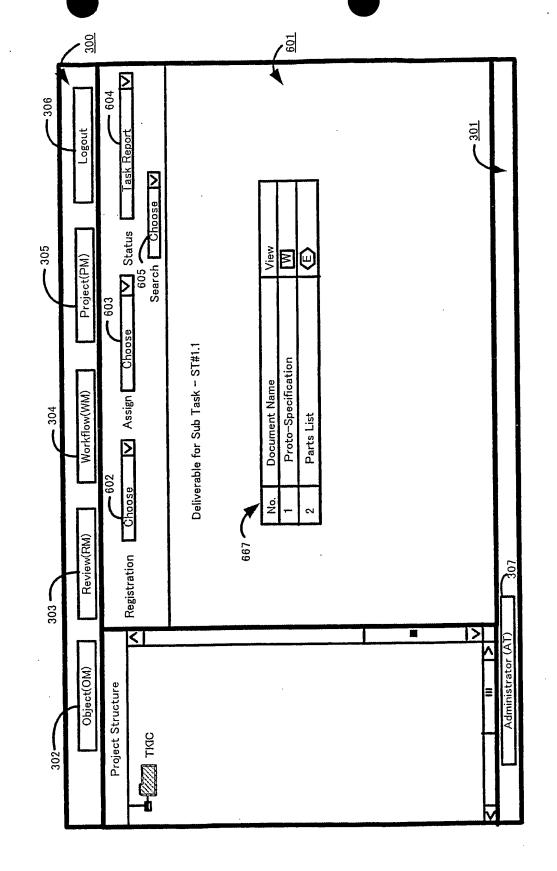


604 Template Temp#3 Temp#3 Temp#4 Temp#2 Temp#2 Temp#1 -ogout Status Task 605 Search Choose V 1,800 Cost 2,200 999 11/10/2002 20:00 Actual End Time 305 i ı Project(PM) 603 11/08/2002 9:00 11/07/2002 10:32 Start Time V Assign Choose Actual Workflow(WM) 304 End Time 11/10/ 2002 Ecpected 11/09/ 2002 ŧ -602 Duration Days 1 8 က Review(RM) Registration 307 Start Time 11/07/ 2002 Expected /11/0/11 2002 t t Administrator (AT) Object(OM) Project Structure H THIC

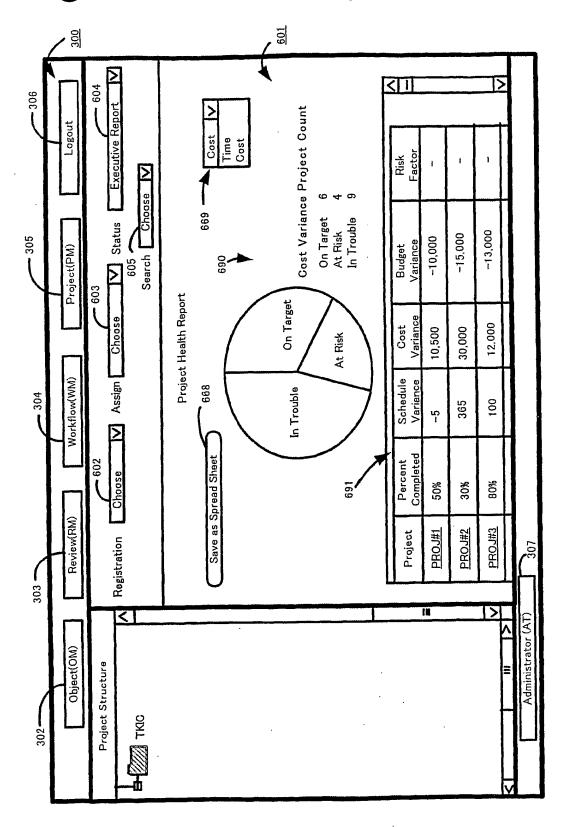
601

FIG. 49

FIG. 50







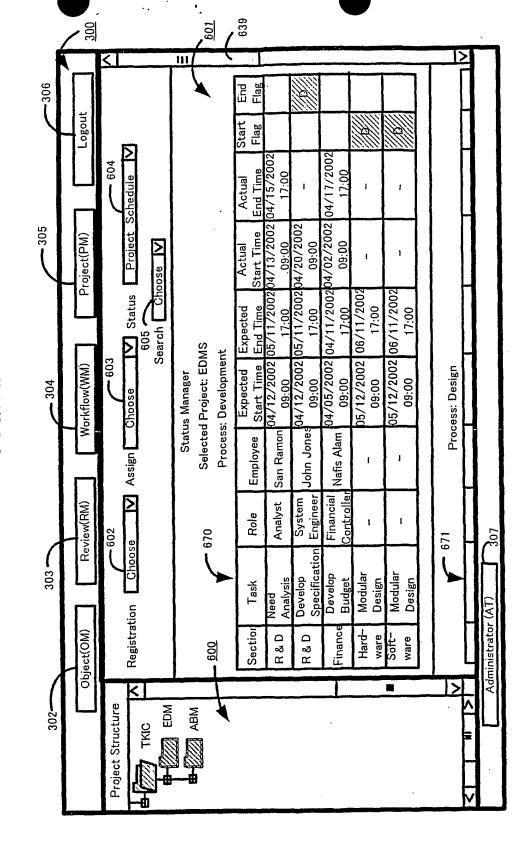


FIG.52

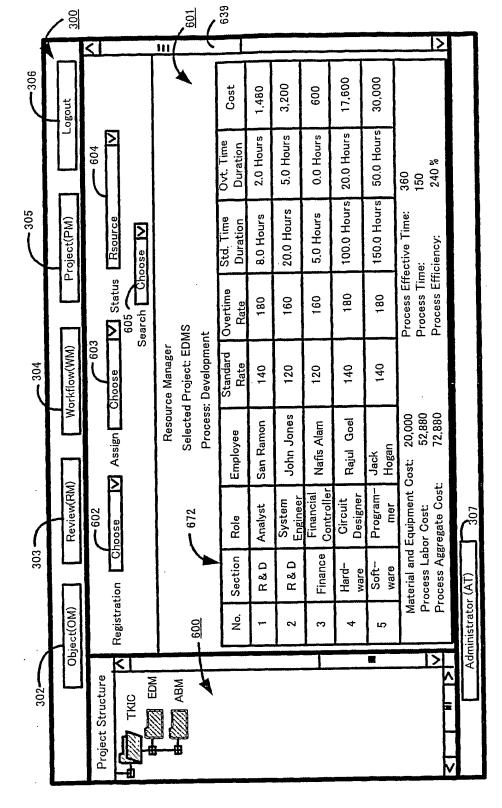


FIG. 53

10/529077 639 300 . 601 683 -306 682M 682 V Status Pie Chart (Cost & Time) - 604 Section Employee Section Process Task Pie Chart: Section - Time -305 Search Choose V ğ 681 Time Fime 680M Cost 685 **•** 603 Norkflow(WM) 304 680 684 Time[%] Selected Project: EDMS M Assign 35 15 Pie Chart: Section - Time Review(RM) Section Name - 602 Software Hardware Finance R&D Registration 4 Pie Chart 3 Object(OM) 009 Project Structure EDM May ABM TKIC 302

FIG.54



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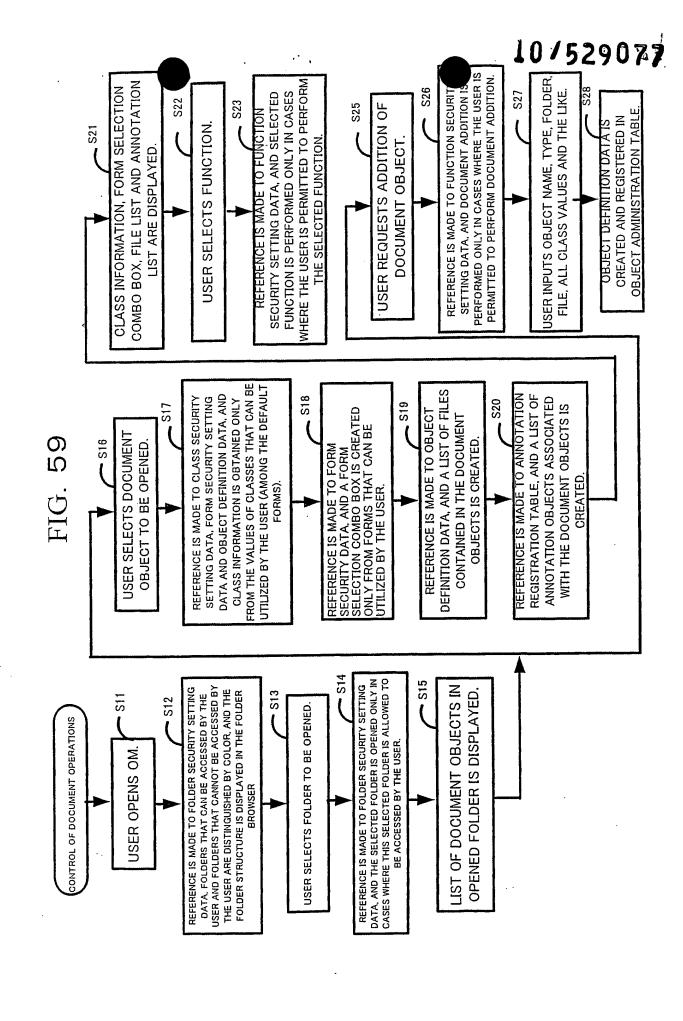
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FLD_a	USR_b	FNC_aa	USR_b	FRM_b	USR_b		CLS_a	USR_b
FLD_c	USR_a	FNC_ac	USR_a	FRM_a	USR_b		CLS_c	USR_a
FLD_b	USR_a	FNC_ab	USR_a	FRM_b	USR_a		CLS_b	USR_a
FLD_a	USR_a	FNC_aa	USR_a	FRM_a	USR_a		CLS_a	USR_a
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10/529077 The same of the same W R. W. ¥FLD\_r¥FLD\_s¥F1L\_t ¥FLD\_r¥FLD\_s¥FIL\_u ¥FLD\_i¥FLD\_j¥F1L\_k ¥FLD\_p¥F1L\_a APPROVAL DATE AND TIME CREATION DATE AND TIME 2003/5/2 15:10:43 2003/5/1 10:15:16 2003/5/1 11:15:34 2003/5/1 10:07:08 : APPROVER USR\_x DID\_aa qq did AUTHOR USR\_c USR\_b USR\_a : REATION DATE 2003/5/1 10:07:08 : : CL\_b VALUE MARKETING ANTHOR PART DESIGN SYSTEM · PLANS USR\_a : DOC\_ADT REATION DATE AND TIME CIRCUIT
DESIGN
DRAWINGS
MINUTES OF
FIRST ANT\_ADT FILE CL\_a VALUE 2003/6/1 SYSTEM MEETING COMMENTS XXXXXX : AUTHOR USR\_a 된 : DOCUMENT ID DOCUMENT ID WKLADT FOLDER DID\_aa FLD\_c : FLD\_b FLD\_a : FIG.58 ANNOTATION NAME WORK LIST NAME TYPE : WKLa SS æ ANT\_a : DOCUMENT DOCUMENT ID DOC\_ab DOC\_ac DOC\_aa ANNOTATION ID WORK LIST ID WLID\_aa AID\_aa : : DID\_ac DID\_aa DID\_ab :



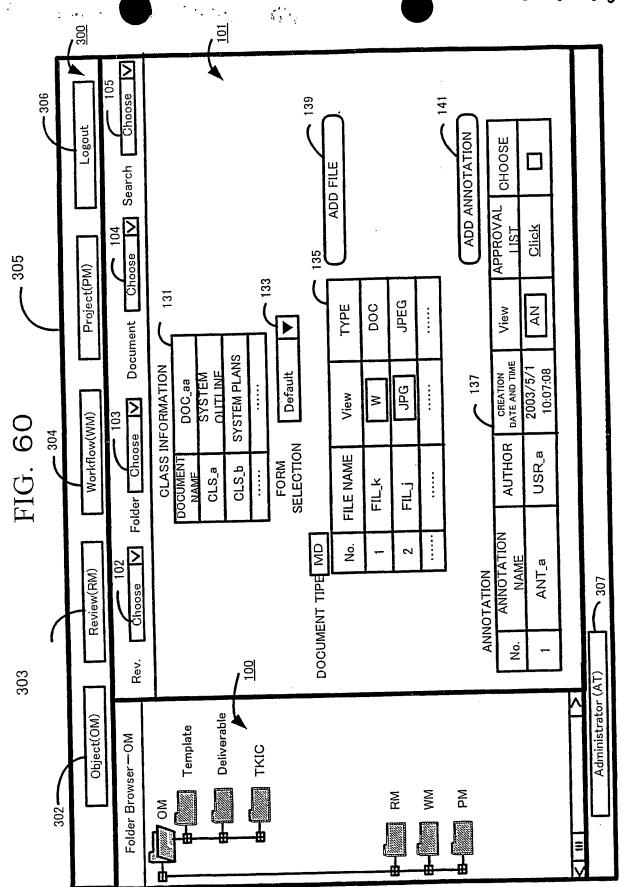
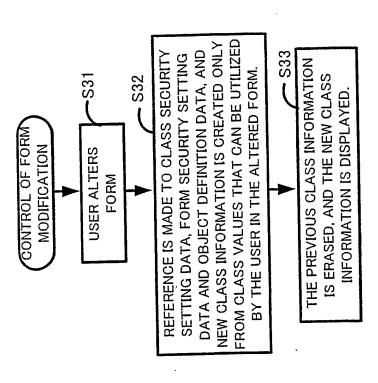


FIG. 6



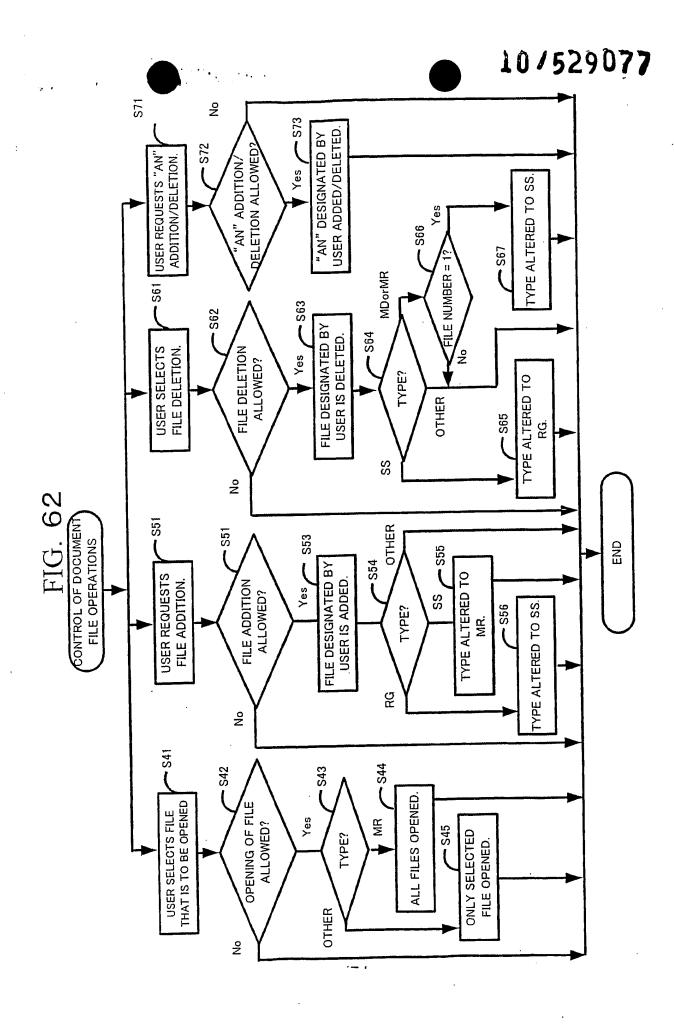
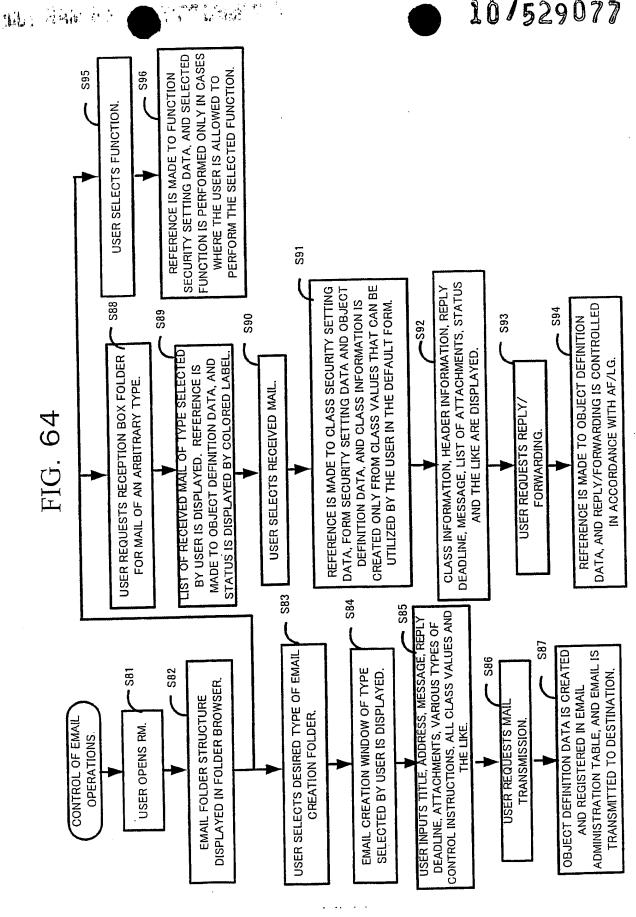


FIG. 63

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	¥FOL_x¥FIL_a	DID_a	WKL_a			r h	<b>1</b>	
		1				STATUS	BEFORE DEADLINE	1
	MESSAGE ATTACHMENT			:		TRANSMISSION DATE AND TIME	2003/7/1 10:07:08	2003/7/1 10:15:16
	MESSAG	XXXXXX	xxxxx	:		TRUNK	OFF	OFF
	CL_a VALUE	××××	×××	:		ARCHIVE	NO	OFF
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	TYPE	FM	IFM			FORWARDING APPROVAL (AE)	NO	NO
	TITLE	EML_aa	EML_ab			FORW		
	MAIL ID	EML_aa	EML_ab	. :			'	*****



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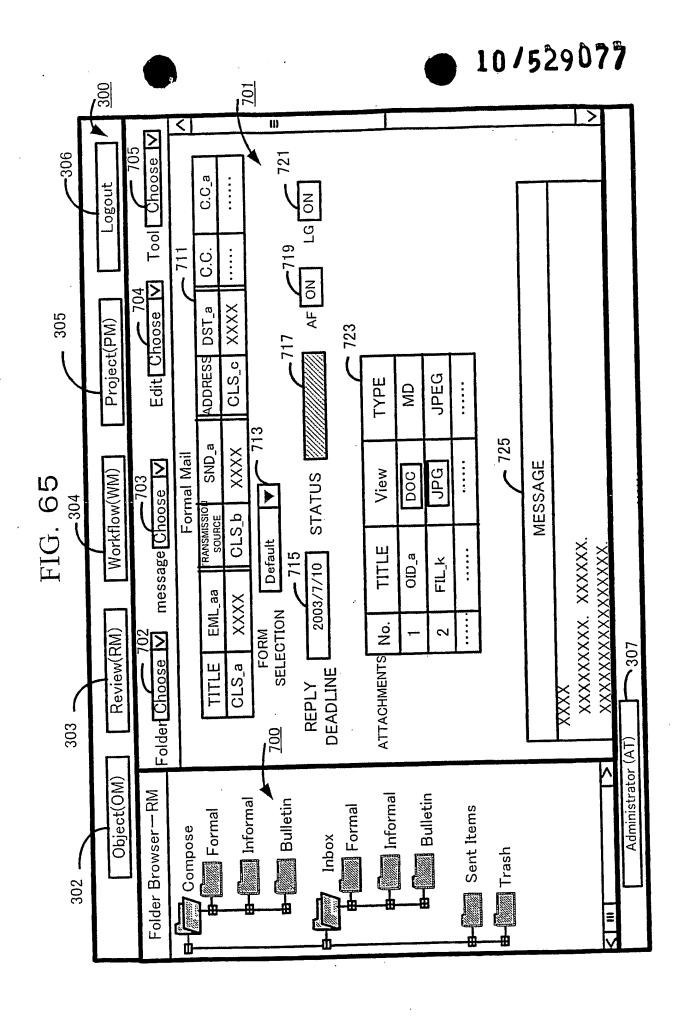
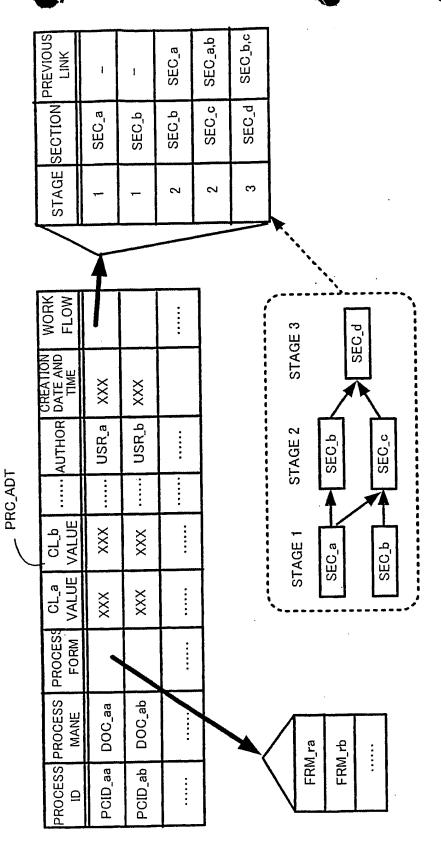


FIG. 66



CREATION DATE AND TIME 2003/5/1 10:07:08 2003/5/1 10:15:16 TEMPLATE AUTHOR USR\_a USR\_b : DID\_ta DID\_tb : FIG. 67 ✓ TSK\_ADT CL\_b VALUE XXXX XXX : CL\_a VALUE XXX × TSK\_ab TSK\_aa TASK NAME TID\_aa TASK ID TID\_ab



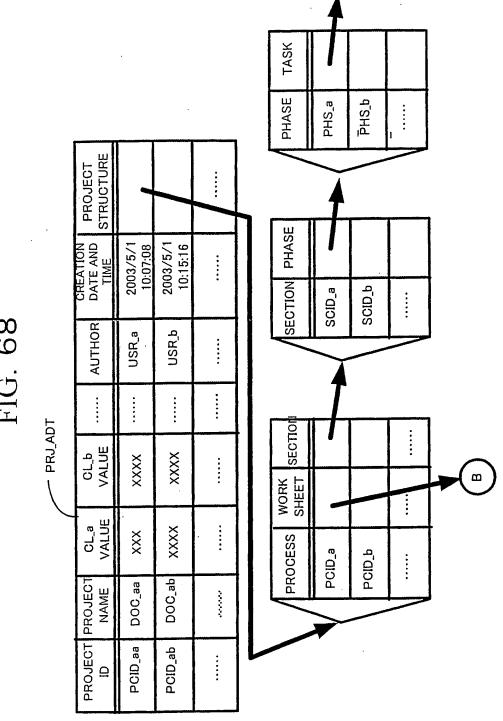


FIG. 68

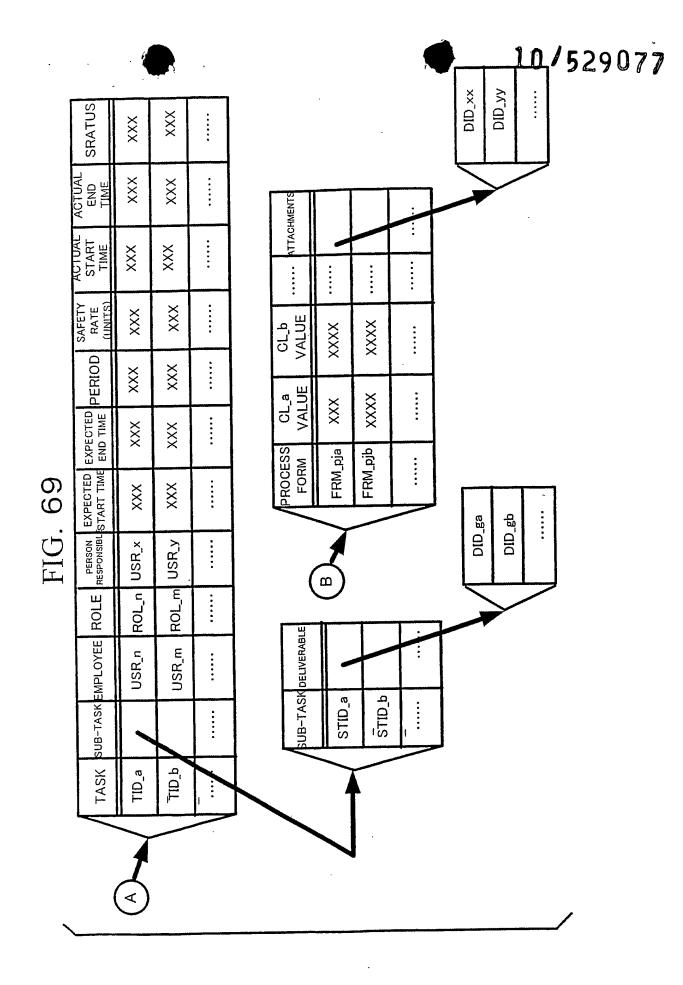
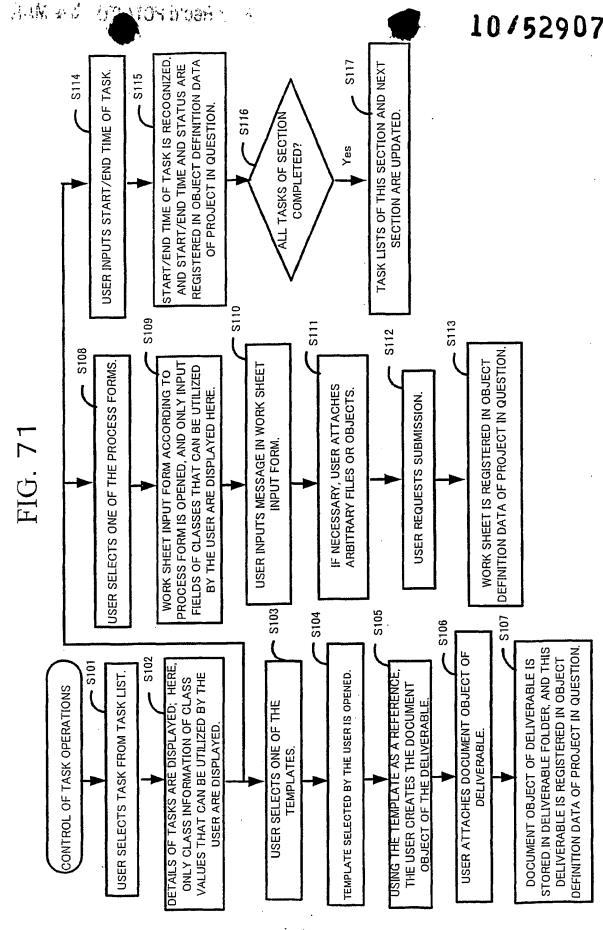


FIG. 70

	TID_a	TID_b		
	TASK DATE AND TIME	2003/6/1 13:17:08	:	
	TASK	_		
TKL_ADT	USER	SCID_a		
	TASK LIST NAME	. TKL_a		
	TASK LIST ID	TLID_aa	:	



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